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A. Nakagawa et al. invented the device design concept of non-latch-up IGBTs in 1984. The invention is characterized by the device design setting the device saturation current below the latch-up current, which triggers the parasitic thyristor. This invention realized complete suppression of the parasitic thyristor action, for the first time, because the maximal collector current was limited by the saturation current and never exceeded the latch-up current.

## **Insulated-gate bipolar transistor - Wikipedia**

Practical insulated gate bipolar transistor (IGBT) devices have a finite size with a well-defined active area where the current flow occurs, an edge termination region surrounding the active area, and pads for locating the wires to carry current into and out of the chip. The design of the active area is related to the on-state current density.

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The Insulated Gate Bipolar Transistor (IGBT) is a minority-carrier device with high input impedance and large bipolar current-carrying capability. Many designers view IGBT as a device with MOS input characteristics and bipolar output characteristic that is a voltage-controlled bipolar device.

## **Insulated Gate Bipolar Transistor (IGBT) Basics**

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